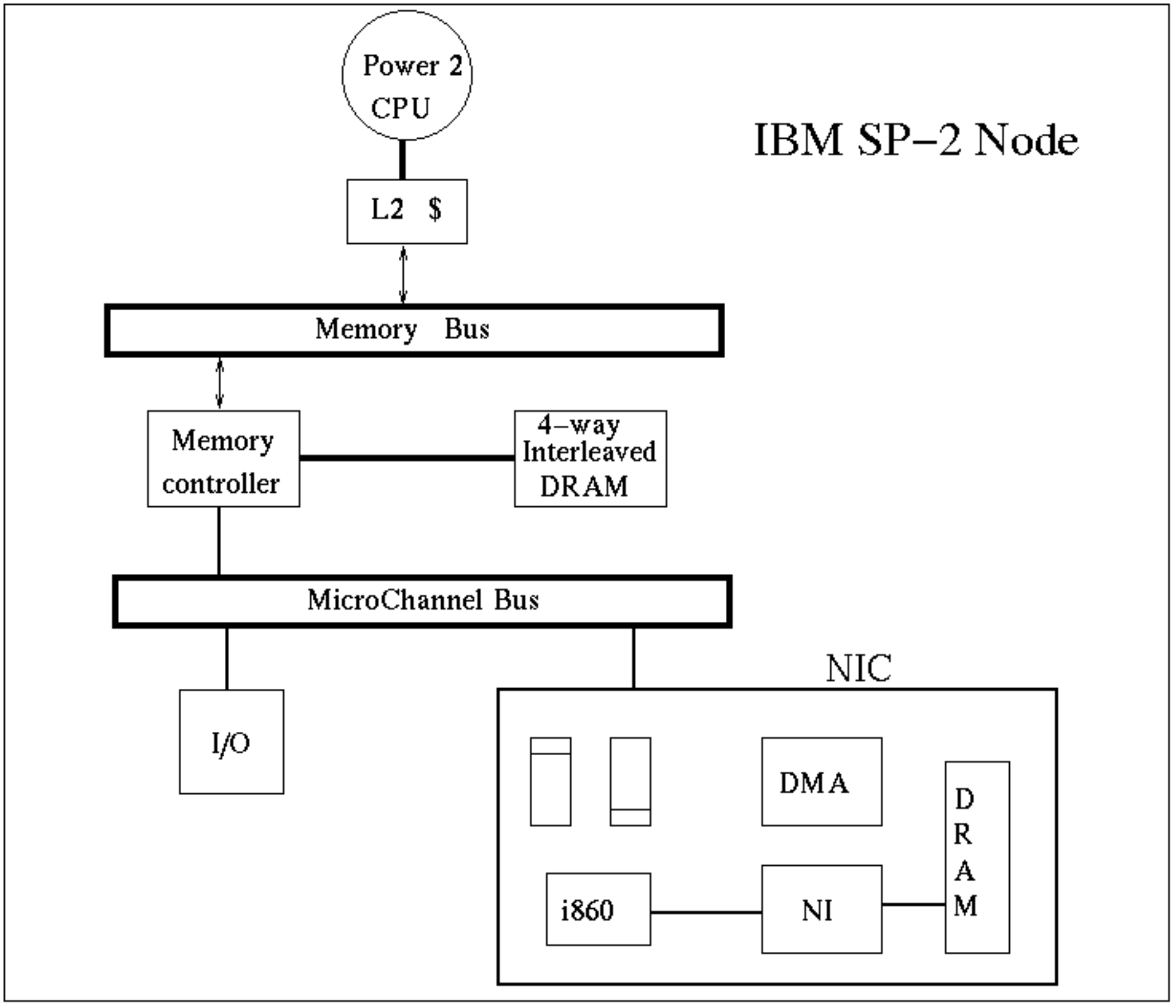
Power 2
CPU
IBM SP–2 Node
L2 $
Memory Bus
Memory
controller
4–way
Interleaved
DRAM
MicroChannel Bus
NIC
I/O
DMA
D
R
A
M
i860
NI