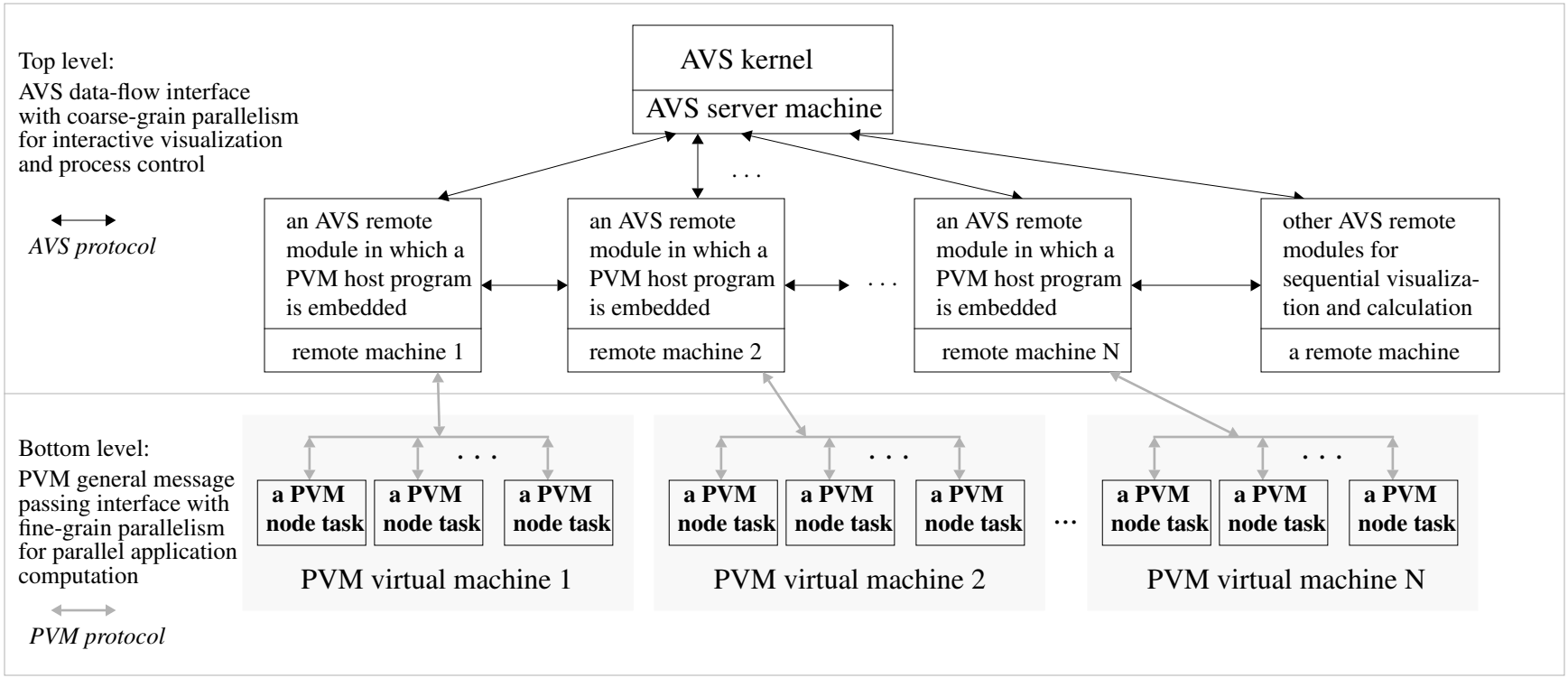

Figure 1: A parallelism hierarchy in the AVS/PVM system
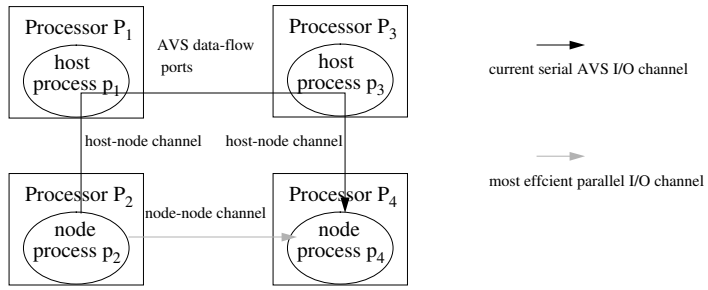
Figure 3: Serial AVS I/O ports versus parallel channels