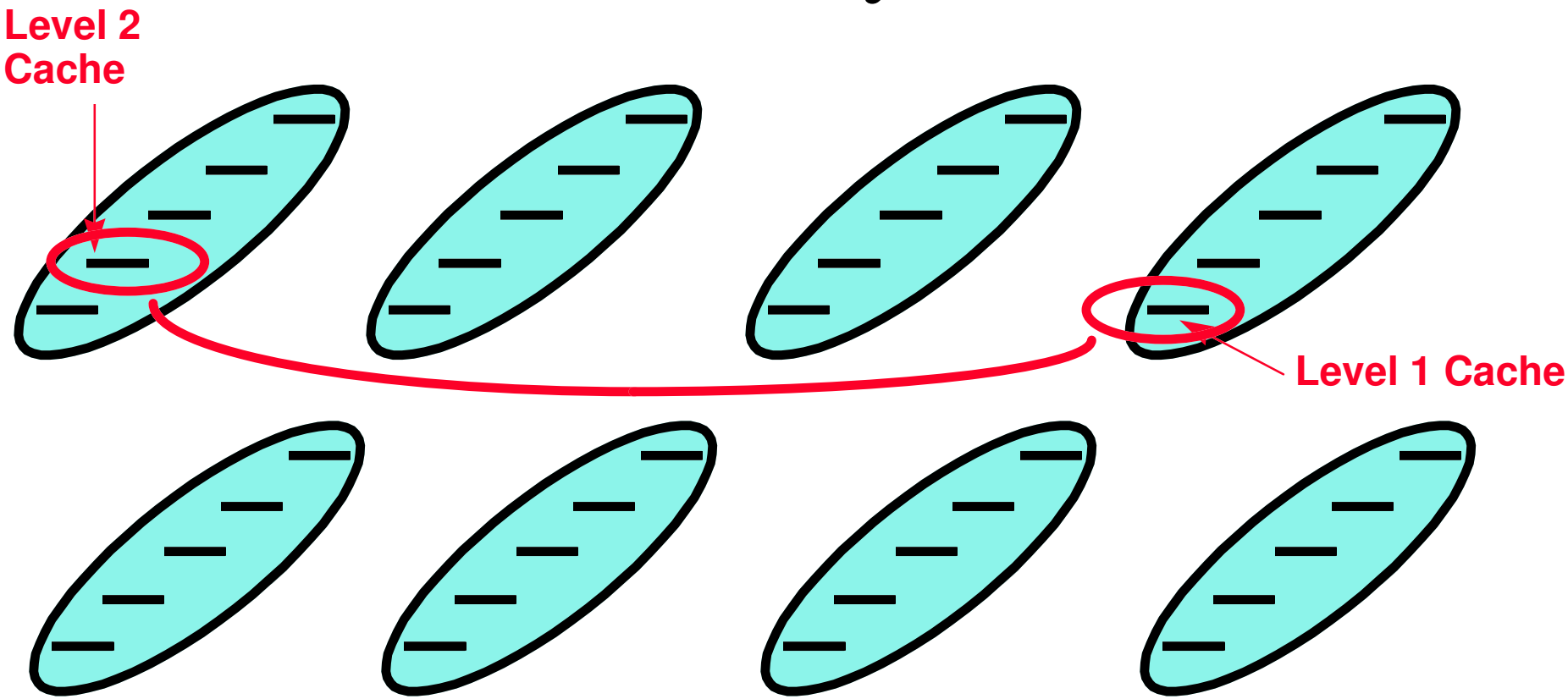Fig 1(b) Data Movement with Multiple Levels of Memory Hierarchy