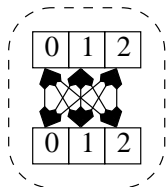


Processor 0

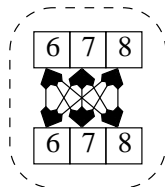
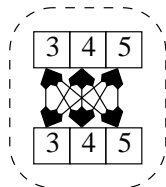
Processor 1

Processor 2

a

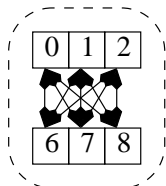


b

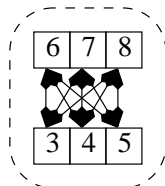
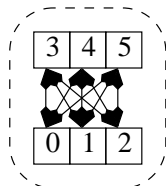


Iteration 0

a

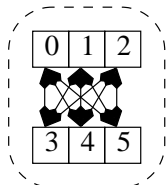


b

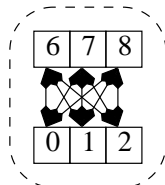
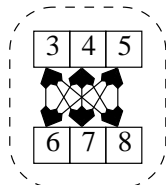


Iteration 1

a



b



Iteration 2