Guansong Zhang

Address:

NPAC, Syracuse University, 111 College Place, Syracuse, NY, 13244, U.S.A.

Education:

B.E. in Computer Science and Engineering from Harbin Institute of Technology, Harbin, China, (1986-1990).

Ph. D. in Computer Science from Harbin Institute of Technology, Harbin, China, (1986-1990).

Professional Experience:

1995-: Research scientist, NPAC, Syracuse University

1992-1995: Teaching Assistant, CS Dept., Harbin Institute of Technology

Awards and Honors:

Anchongen scholarship for outstanding postgraduate student (HIT, 1995)

Earlier Research Projects:

Data Parallel Processing Based on Networked Workstations (1994-1995, Grant from the Commission of National Defence Industry)

Multiprocessor Performance Evaluation Techniques (1991-1993, Grant from the Commission of National Defence Industry, China)

Distributed Computing Architecture (1989-1992, Grant from the Ministry of Aerospace, China)

Research Projects in progress:

Frontend system implementation and integration.

HPF compiler and PCRC run time library.

Development of the HPJava translator.

Selected List of Publications:

- [1] Xiaoming Li, Guansong Zhang, et al, "HPFfe: a Front-end for HPF", Technical Report, SCCS-771, NPAC at Syracuse University, 1996.4.
- [2] Guansong Zhang, "Partitioning Data Parallel Program for Workstation Cluster", Ph.D thesis, Harbin Institute of Technology., 1995

- [3] Guansong Zhang, Xiaoming Li, "Barrier Synchronization and Pipeline Synchronization in Distributed Memory Machines", Science Bulletin, 1996
- [4] Guansong Zhang, Xiaoming Li, "Predicting Execution Time of Parallel Program Based on Simulation Software", Chinese Journal on Computers., 1995
- [5] Guansong Zhang, Xiaoming Li, "Analysis of Interconnection Functions Needed for a New Nonlinear Skewing Scheme", Chinese Journal on Computers., 1994
- [6] Guansong Zhang, et al, "Software System for ABC-90jr., an Array Based Computer", IEEE TENCON'93, 1993

Ph.D Advisor:

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