Concurrency: Practice and Experience
Papers Published — 1996

(C156) "Modeling the CM-5 Multicomputer"

Authors: Zeki Bozkus, Sanjay Ranka, and Geoffrey Fox

Current Status: Paper to Publisher (2/9/95) — Vol. 8, No. 1.

Short Code: [Dincer:96a]

(C165) "Towards a Complete Framework for Parallel Implementation of Logic Languages: The Data Parallel Implementation of SEL"

Authors: Giancarlo Succi and Carl Uhrik

Current Status: Paper to Publisher (3/13/95) — Vol. 8, No. 3.

Short Code: [Succi:96a]

(C201) "Building a Global Clock for Observing Computations in Distributed Memory Parallel Computers"

Authors: Jean-Marc Jezequel and Claude Jard

Current Status: Paper to Publisher (2/9/95) — Vol. 8, No. 1.

Short Code: [Jezequel:96a]

(C214) "Portable Parallelizing Fortran Compiler"

Authors: A. Averbuch, R. Dekel, E. Gabber

Current Status: Paper to Publisher (2/9/95) — Vol. 8, No. 2.

Short Code: [Averbuch:96a]

(C218) "Effective Data Parallel Computation using the Psi Calculus"

Authors: Lenore M. R. Mullin and Michael A. Jenkins

Current Status: Paper to Publisher (9/19/95) — Vol. 8, No. 7.

Short Code: [Mullin:96a]

(C230) "Dynamics Simulation of Multibody Chains on a Transputer System"

Authors: B. Pond and I. Sharf

Current Status: Paper to Publisher (3/13/95) — Vol. 8, No. 3.

Short Code: [Pond:96a]

(C232) "Performance Modelling of Three Parallel Sorting Algorithms on a Pipelined Transputer Network"

Authors: V. Lakshmi Narasimhan, and J. Armstrong

Current Status: Paper to Publisher (3/14/95) — Vol. 8, No. 7.

Short Code: [Narasimhan:96a]

(C234) "Reliable Parallel Software Construction using PARSE"

Authors: I. Gorton, I. Jelly, J. Gray, and T. S. Chan

Current Status: Paper to Publisher (2/9/95) — Vol. 8, No. 2.

Short Code: [Gorton:96a]

(C235) "An Experiment to Measure the Usability of Parallel Programming Systems"

Authors: D. Szafron, and J. Schaeffer

Current Status: Paper to Publisher (2/17/95) — Vol. 8, No. 2.

Short Code: [Szafron:96a]

(C236) "Convergence of Concurrent Markov-Chain Monte Carlo Algorithms"

Authors: M. Malfait, and D. Roose

Current Status: Paper to Publisher (2/9/95) — Vol. 8, No. 3.

Short Code: [Malfait:96a]

(C237) "Fail-Safe Concurrency in the *EcliPSe* System"

Authors: Felipe Knop, Vernon Rego, and Vaidy Sunderam

Current Status: Paper to Publisher (6/6/95) — Vol. 8, No. 4.

Short Code: [Knop:96a]

(C238) "SPEED: A Parallel Platform for Solving and Predicting the Performance of PDEs on Distributed Systems"

Authors: Chi-Chung Hui, Mounir Hamdi, and Ishfaq Ahmad

Current Status: Paper to Publisher (6/6/95) — Vol. 8, No. 7.

Short Code: [Hui:96a]

(C239) "PB-BLAS: A Set of Parallel Block Basic Linear Algebra Subprograms"

Authors: J. Choi, J. Dongarra, and D. Walker

Current Status: Paper to Publisher (6/6/95) — Vol. 8, No. 7.

Short Code: [Choi:96a]

(C241) "Native and Generic Parallel Programming Environments on a Transputer Platform"

Authors: A. G. Hoekstra, F. van der Linden, M. van Muisurnkel, J. J. J. Vesseur, L. O. Hertzberger, and P. M. A. Sloat

Current Status: Paper to Publisher (1/24/95) — Vol. 8, No. 1.

Short Code: [Hoekstra:96a]

(C242) "A Parallel Algorithm for the Integer Knapsack Problem"

Authors: Daniel Morales, J. L. Roda, Casiano Rodriguez Leon, Francisco Almeida, and Felix Garcia

Current Status: Paper to Publisher (3/14/95) — Vol. 8, No. 4.

Short Code: [Morales:96a]

(C245) "MP: An Application Specific Concurrent Language"

Authors: J. S. Reeve and M. C. Rogers

Current Status: Paper to Publisher (6/6/95) — Vol. 8, No. 4.

Short Code: [Reeve:96a]

(C246) "Do-Loop-Surface: An Abstract Representation of Parallel Program Performance"

Authors: Oscar Naím, A. J. G. Hey, and Ed Zaluska

Current Status: Paper to Publisher (3/14/95) — Vol. 8, No. 3.

Short Code: [Naim:96a]

(C252) "Parallel DSP Algorithms on Turbo Net: An Experimental System with Hybrid Message-Passing/Shared-Memory Architecture"

Authors: X. Li, S. G. Ziavras, and C. N. Manikopoulos

Current Status: Paper to Publisher (5/11/95) — Vol. 8, No. 5.

Short code: [Li:96a]

(C253) "Performance Analysis of Distributed Implementations of a Fractal Image Compression Algorithm"

Authors: David Jeff Jackson and Greg Scott Tinney

Current Status: Paper to Publisher (6/6/95) — Vol. 8, No. 5.

Short Code: [Jackson:96a]

(C257) "Exploiting Application Parallelism in Knowledge-base Systems: An Experimental Method"

Authors: J. W. H. Daniel and M. R. Moulding

Current Status: Paper to Publisher (12/5/94) — Vol. 8, No. 1.

Short Code: [Daniel:96a]

(C265) "On Estimating the useful Work Distribution of Parallel Programs under the P^3T: A Static Performance Estimator"

Authors: Thomas Fahringer

Current Status: Paper to Publisher (3/14/95) — Vol. 8, No. 4.

Short Code: [Fahringer:96a]

(HPCS-6) "The W-Network: A Low-cost Fault-tolerant Multistage Interconnection Network for Fine-grain Multiprocessing"

Authors: K. B. Theobald

Current Status: Paper to Publisher (2/2/96) — Vol. 8, No. 6.

Short Code: [Theobald:96a]

(HPCS-1) "High-performance Computing using a Reconfigurable Accelerator"

Authors: R. W. Hartenstein, J. Becker, R. Kress, and H. Reinig

Current Status: Paper to Publisher (2/2/96) — Vol. 8, No. 6.

Short Code: [Hartenstein:96a]

(HPCS-2) "Scanning Parameterized Polyhedron using Fourier-Motzkin Elimination"

Authors: M. Le Fur

Current Status: Paper to Publisher (2/2/96) — Vol. 8, No. 6.

Short Code: [Fur:96a]

(HPCS-5) "A Family of Parallel QR Factorization Algorithms"

Authors: G. L. Meyer, and M. Pascale

Current Status: Paper to Publisher (2/2/96) — Vol. 8, No. 6.

Short Code: [Meyer:96a]

(HPCS-3) "Visibility Analysis on a Massively Data-parallel Computer"

Authors: D. Germain, D. Laurendeau, and G. Vézina

Current Status: Paper to Publisher (2/2/96) — Vol. 8, No. 6.

Short Code: [Germain:96a]

(HPCS-4) "Parallelizing a Powerful monte Carlo Method for Electron Beam Dose Calculations"

Authors: P. G. Kropf, H. Neuenschwander, W. Volken, and P. Schwab

Current Status: Paper to Publisher (2/2/96) — Vol. 8, No. 6.

Short Code: [Kropf:96a]