Manish Parashar

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Research Interests

Parallel languages and programming abstractions, distributed dynamic data-structures, computational infrastructures and problem solving environments for grand challenge applications, software engineering and object-oriented software development, performance evaluation and prediction, and expert systems.

Present Position

- Research Associate/Lecturer, Computer Science, University of Texas, Austin (Oct.'95 - Present)

Previous Positions

- Post-Doctoral Fellow, Computer Science, University of Texas, Austin (Sept.'94 - Sept.'95)
- Research Asst., Northeast Parallel Architectures Center, Syracuse University (Aug.'91 - Aug.'94)
- Special Consultant, Booz·Allen & Hamilton, Mountain View, CA (May'93 - June'93)
- Intern, Agway Feed Corporation, Syracuse, NY (Apr.'91 - Aug.'91)
- Research Assistant, ECE Department, Syracuse University (Aug.'90 - Apr.'91)
- Intern, Mentor Graphics, Silicon Design Division, Warren, NJ (May'90 - Aug.'90)

Education

- Ph.D. Computer Engineering, Syracuse University (August '94)
- M.S. Computer Engineering, Syracuse University (May '94)
- B.E. Electronics & Telecommunication Engineering, Bombay University, India (October '88)

Honors and Awards

- Phi Beta Delta Honor Society, Syracuse University, 1994.
- Best Student Paper Award, Supercomputing '92.
- Graduate Scholarship, Syracuse University, 1990-94.
- Best Graduating Student Award, Bombay University, 1988.
- Highest Merit in Electronics & Telecommunications Scholarship, Bombay University, 1988.

Professional Activities

- Member of Program Committee, Ninth International Conference On Parallel and Distributed Computing Systems (PDCS '96).
- Publicity Chair, 5th International Symposium On High Performance Distributed Computing (HPDC-5).
- Member, IEEE & IEEE Computer Society.
- Member, Association for Computer Machinery (ACM).

Recent Relevant Publications

- M. Parashar, J.C. Browne, et. al., *"A Common Computational Infrastructure for Adaptive Algorithms for PDE Solutions,"* submitted for publication.
- M. Parashar and J.C. Browne, *"Object-Oriented Programming Abstractions for Parallel Adaptive Mesh-Refinement,"* Parallel Object-Oriented Methods and Applications (POOMA), Feburary, 1996.
- M. Parashar and J.C. Browne, *"On Partitioning Dynamic Adaptive Grid Hierarchies,"* Proceedings of the 29th Annual Hawaii International Conference on System Sciences, January, 1996.
- M. Parashar and J.C. Browne, *"Distributed Dynamic Data-Structures for Parallel Adaptive Mesh-Refinement,"* Proceedings of the International Conference for High Performance Computing, December, 1995.

There are more than 15 other publications related to high performance, parallel/distributed computing, software development and computational science.